



EXPEDITED PROCEDURE – EXAMINING GROUP 2189

S/N 10/607,772

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Robert Royer et al.	Examiner:	Leonid Kravets
Serial No.:	10/607,772	Group Art Unit:	2189
Filed:	June 27, 2003	Docket No.:	884.905US1
Title:	CACHE WRITE INTEGRITY LOGGING (As Amended)		
Assignee:	Intel Corporation	Customer Number:	21186

AMENDMENT & RESPONSE UNDER 37 C.F.R. 1.116

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Commissioner for Patents
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In response to the Advisory Action mailed February 15, 2006, please amend the claims as follows, and consider the appended remarks. In addition, please note that this Amendment and Response is filed in conjunction with a Request for Continued Examination. This Response is accompanied by a Petition, as well as the appropriate fee, to obtain a one-month extension of the period for responding to the Final Office action, thereby moving the deadline for response from February 21, 2006 to March 21, 2006.

If the Examiner is not persuaded that the pending claims are in condition for allowance after reviewing this document, as well as any art discovered during additional searching activity, the Applicant's representative, Mark V. Muller, respectfully requests the benefit of an Examiner's Interview.